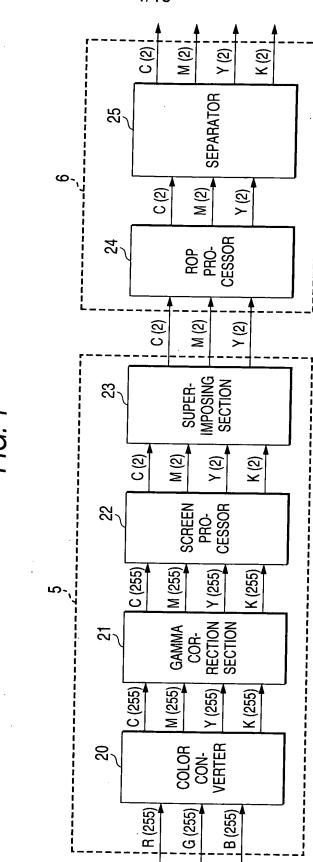


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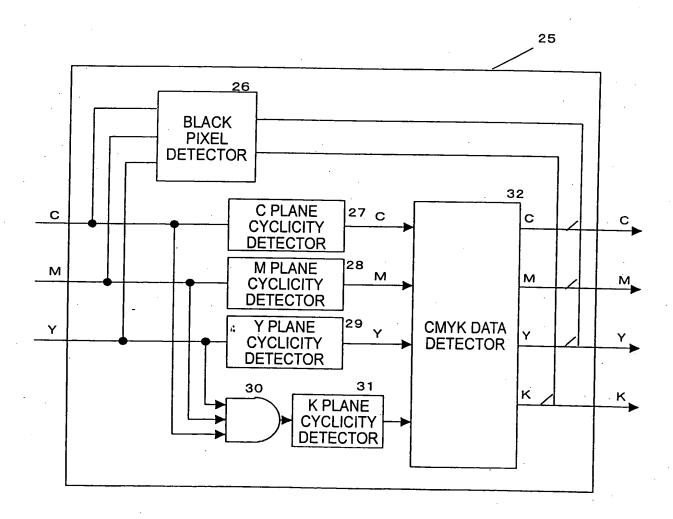
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FIG. 2



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FIG. 3A

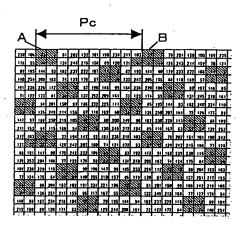


FIG. 3B

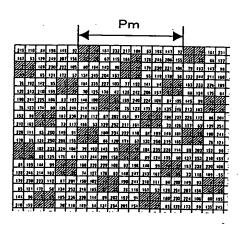


FIG. 3C

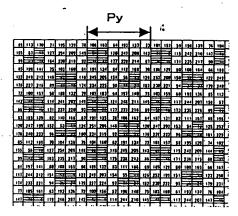
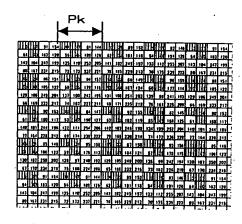


FIG. 3D



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FIG. 4

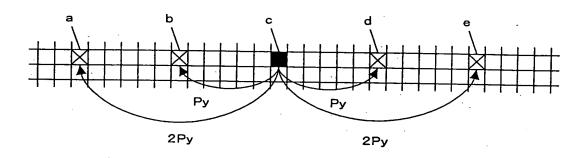


FIG. 5A

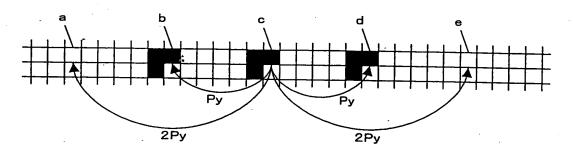
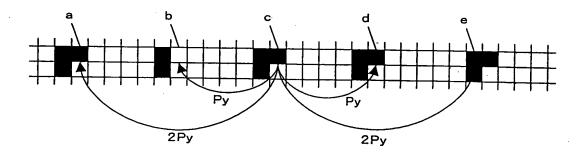


FIG. 5B



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FIG. 6A

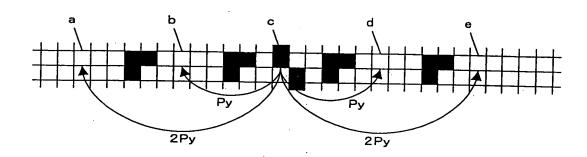
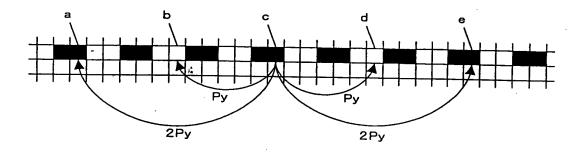


FIG. 6B



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FIG. 7B

FIG. 7C

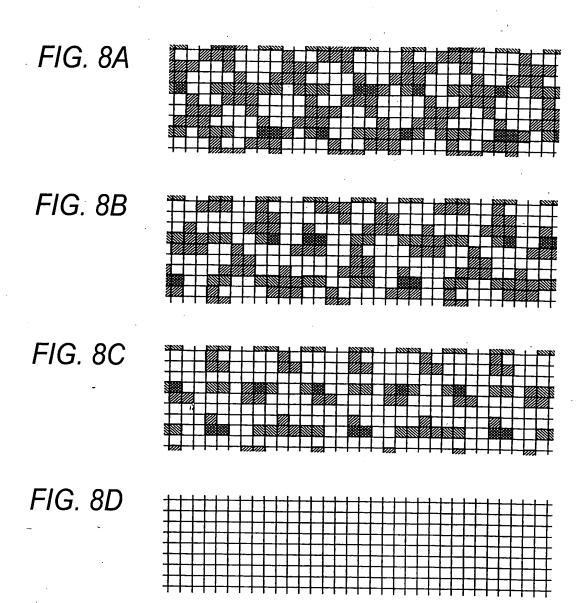
FIG. 7D

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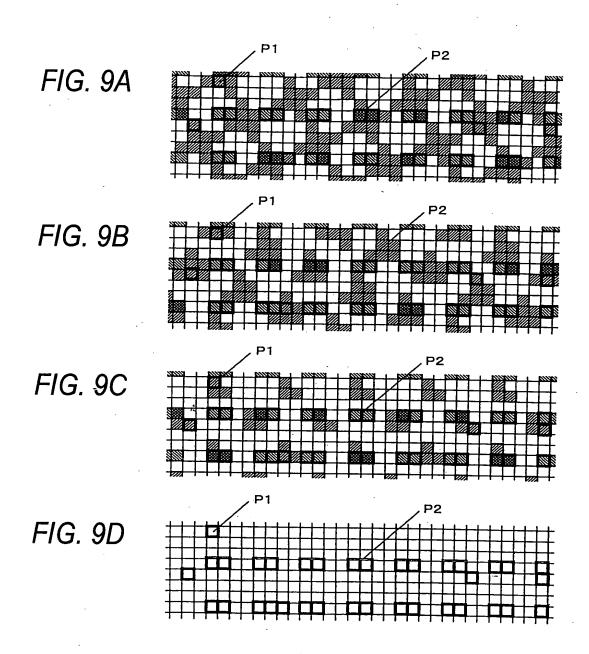


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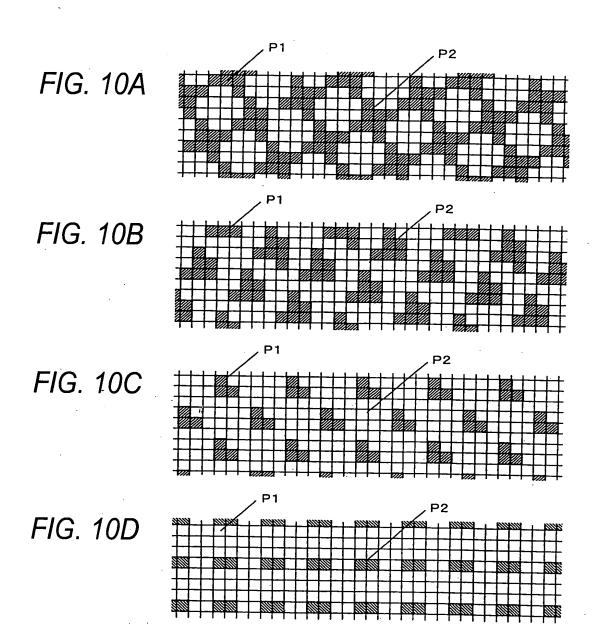
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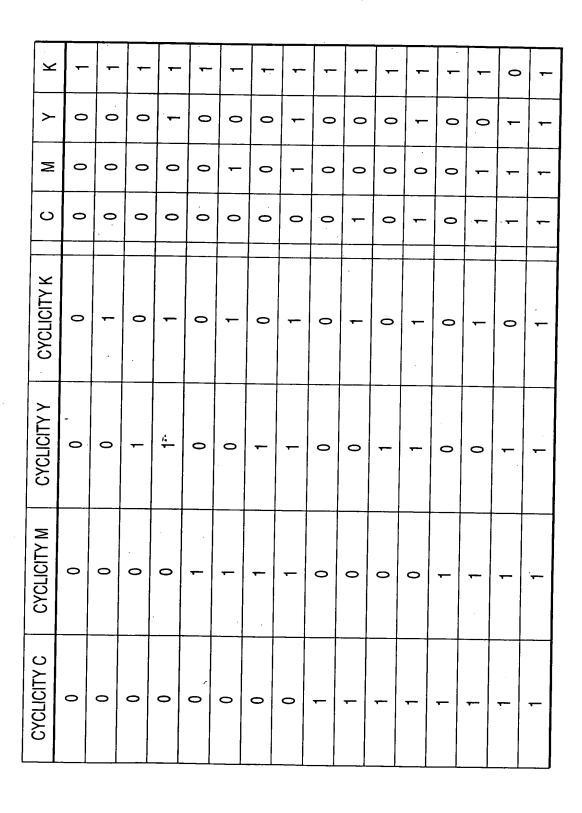
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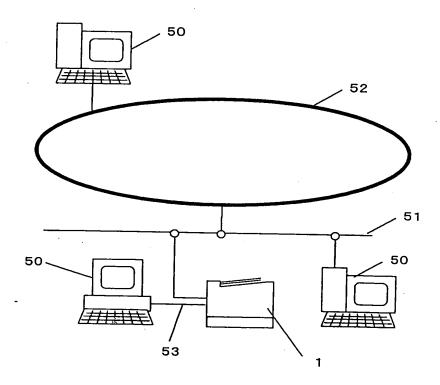


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FIG. 12

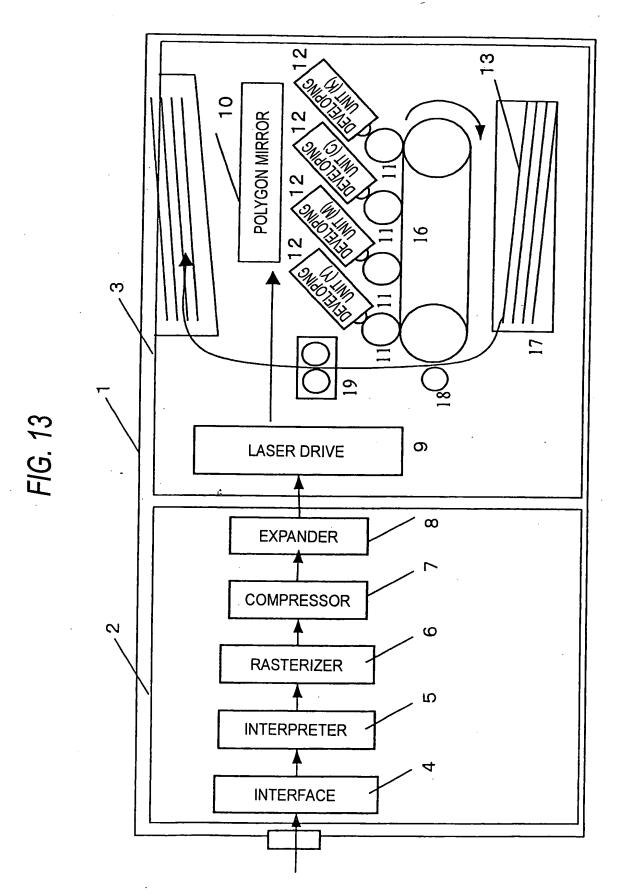


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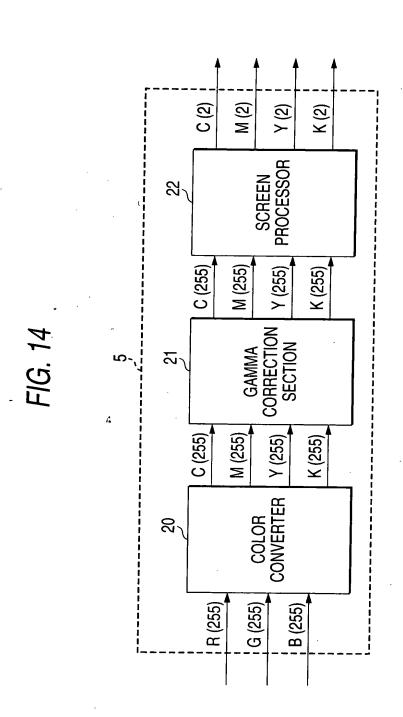
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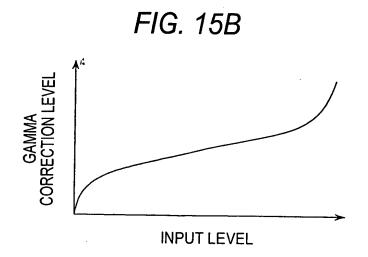
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FIG. 15A **OUTPUT DENSITY INPUT LEVEL**



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FIG. 16A

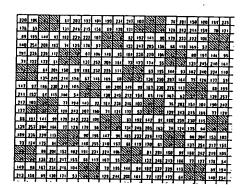


FIG. 16C

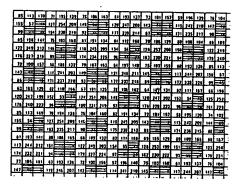


FIG. 16B

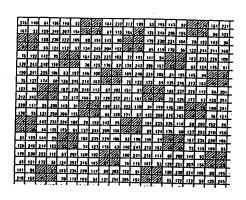
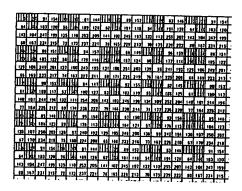


FIG. 16D

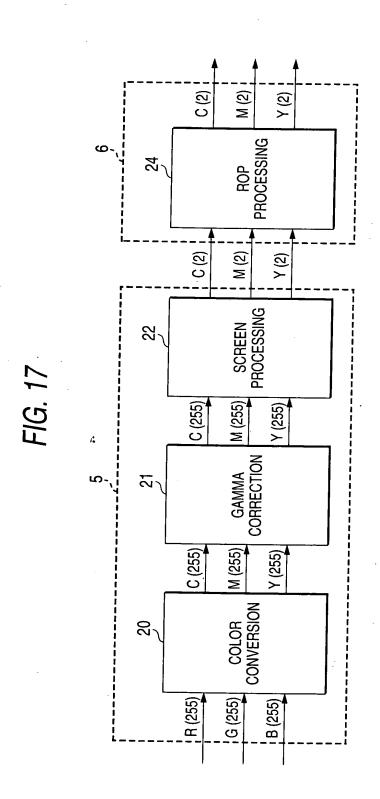


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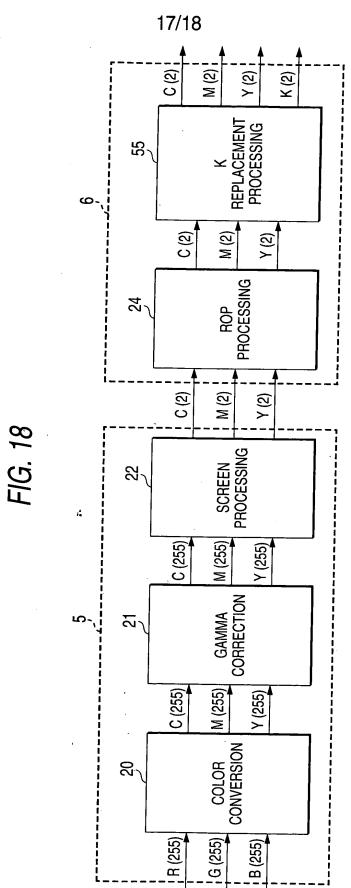
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